

Figure 1

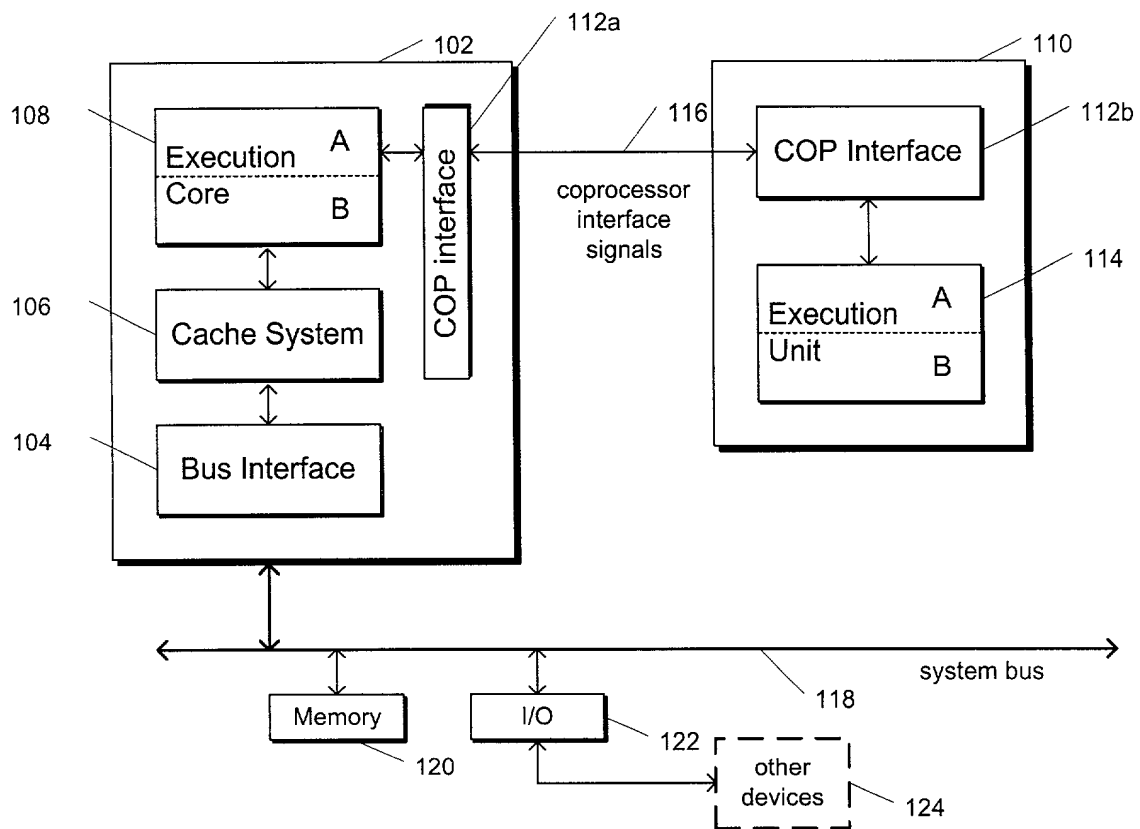


Figure 2

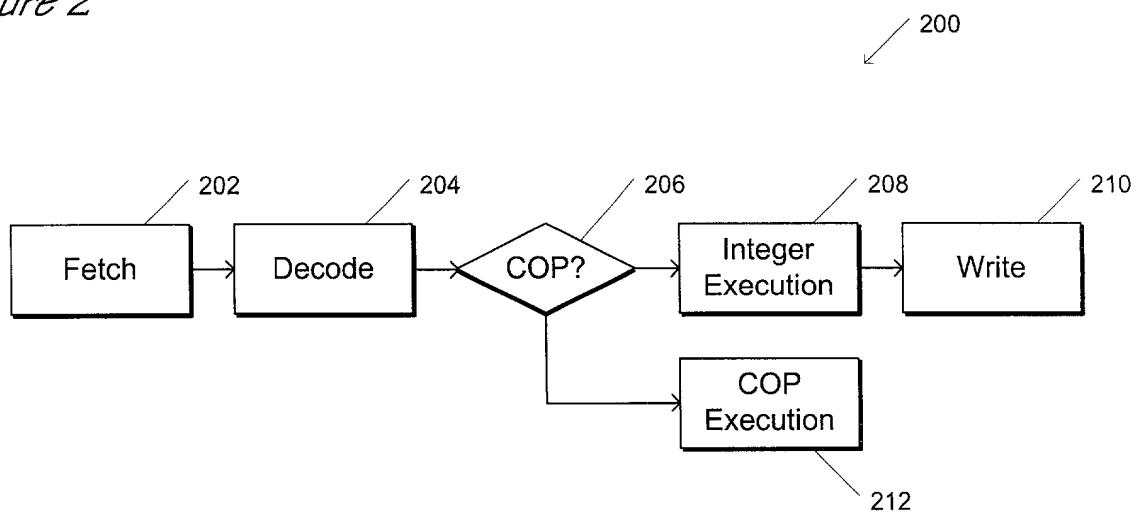


Figure 3

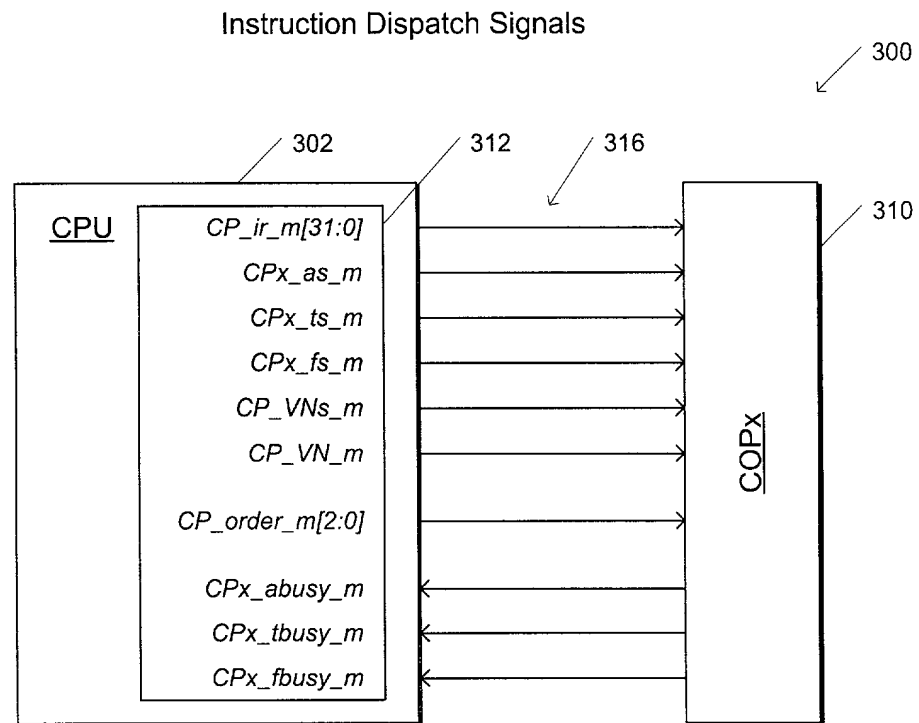
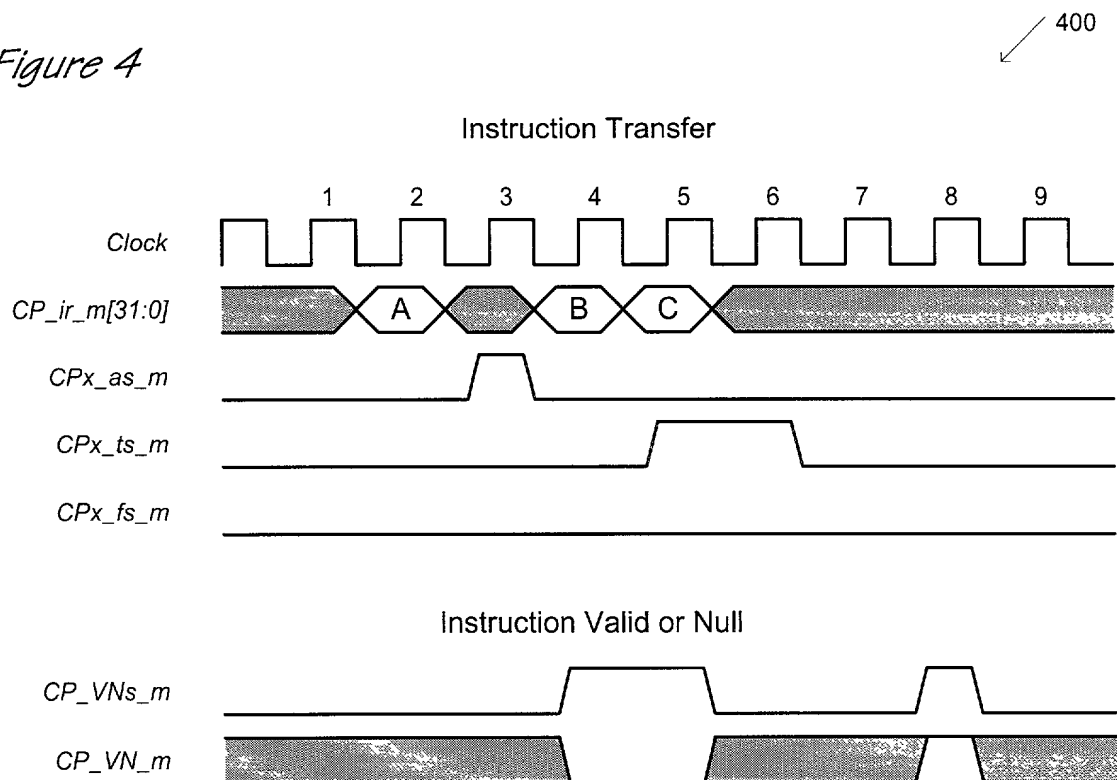


Figure 4



[illegible]

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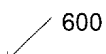


Figure 7

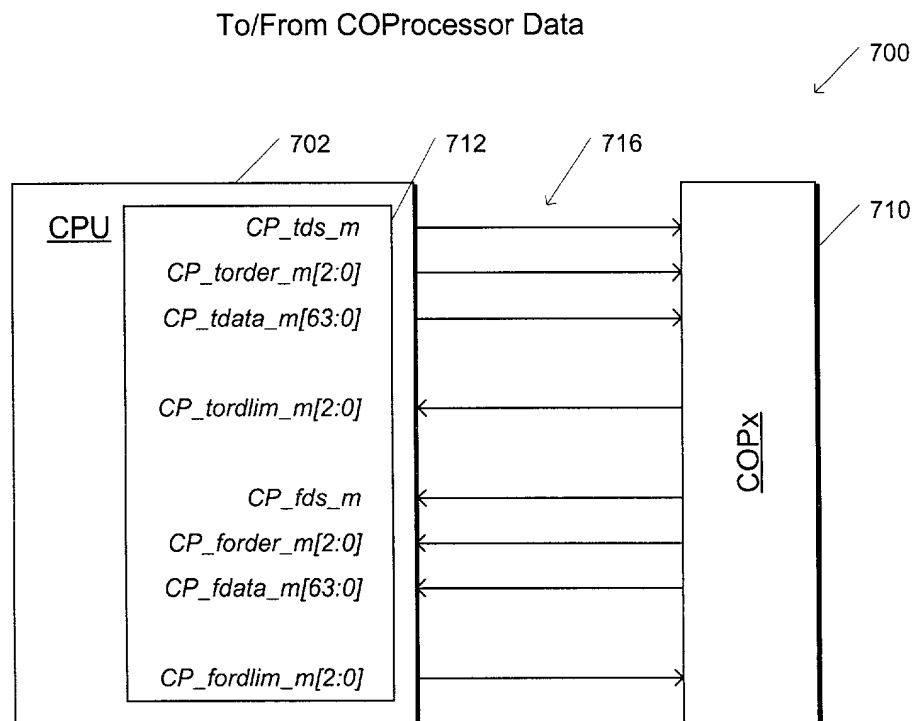


Figure 8

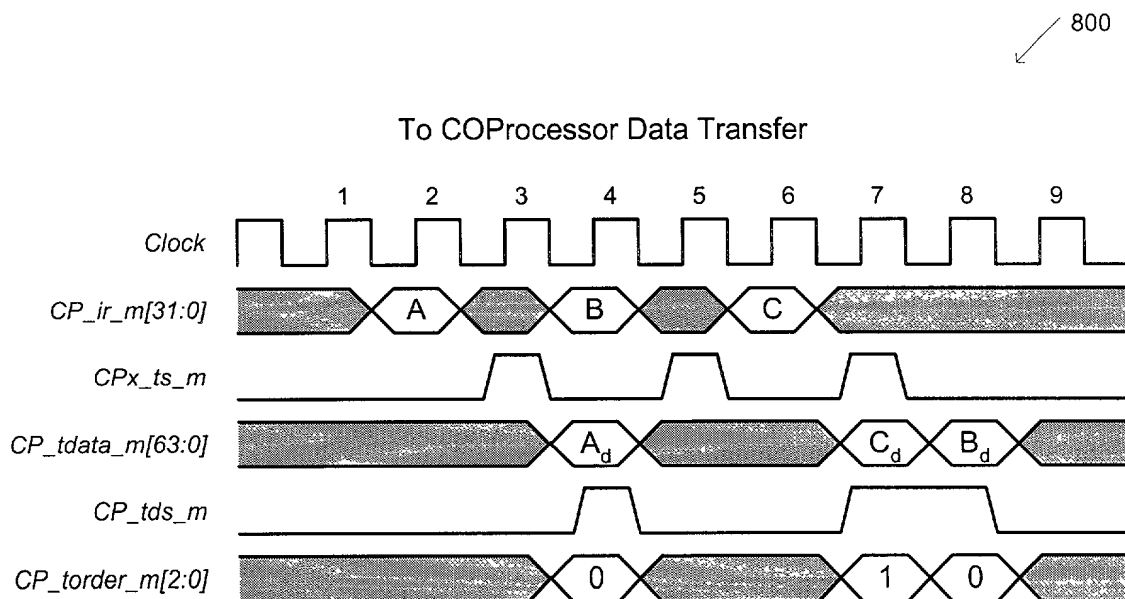


Figure 9

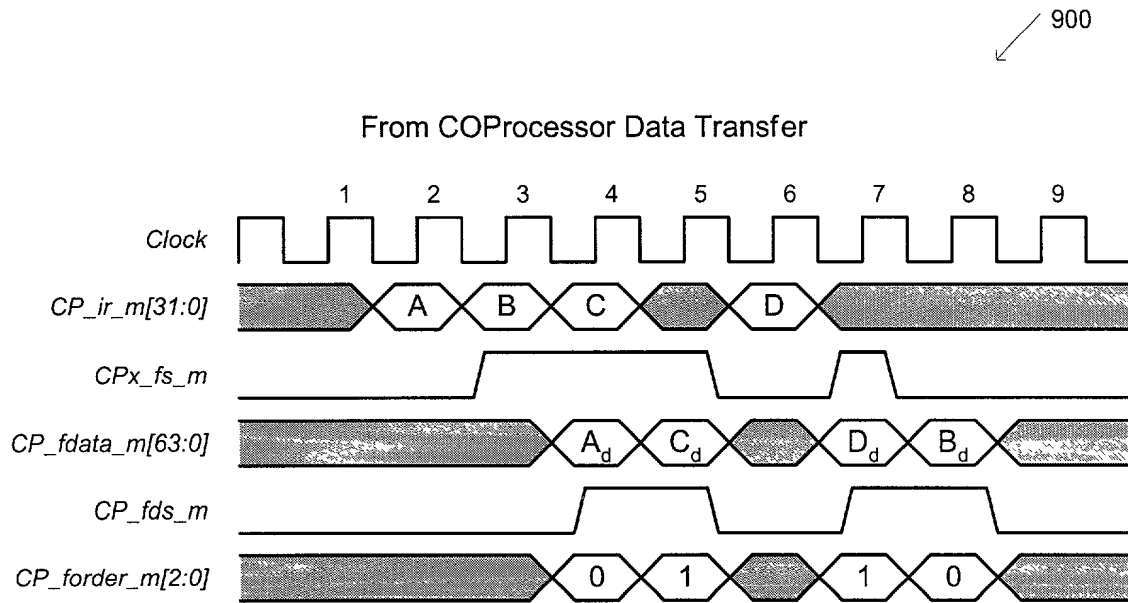
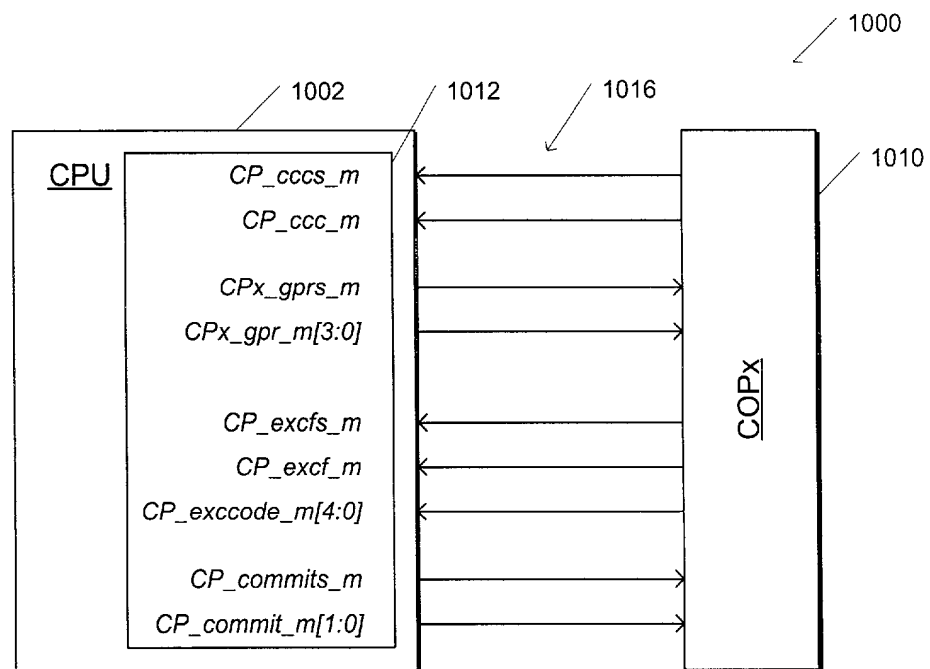


Figure 10

COProcessor Condition Code Check / GPR / exception Found / Instruction Commit



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✓ 1200



Figure 13

